



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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7/14/01
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In re application of:

Bulent Dervisoglu et al.

Application No.: 09/275,726

Filed: March 24, 1999

For: ON-CHIP SERVICE PROCESSOR
FOR TEST AND DEBUG OF
INTEGRATED CIRCUITS

Examiner: D. Ton

Art Unit: 2133

SECOND SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT UNDER
37 CFR §1.97 and §1.98

RECEIVED

Assistant Commissioner for Patents
Washington, D.C. 20231

JUL 13 2001

Technology Center 2100

Sir:

The reference cited on attached form PTO/SB/21 is being called to the attention of the Examiner. A copy of the reference is enclosed. It is respectfully requested that the cited reference be expressly considered during the prosecution of this application, and the reference be made of record therein and appear among the "references cited" on any patent to issue therefrom.

Applicants also submit a fee of \$240.00 under 37 C.F.R. §1.17(p) for submission of this statement. If an additional fee or amount is required, the undersigned attorney respectfully requests the Patent Office contact him immediately at the address below since he has not yet established a Deposit Account with the Patent Office.

Respectfully submitted,

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TO: Examiner David Ton, Art Unit 2133 Fax. No. (703) 308-6296
FROM: Gary T. Aka, Reg. No. 29,038 *gta* Ph. No. (650) 564-9888
DATE: August 10, 2001
RE: Supplemental Amendment for Appln. No.09/275,726

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